Vishay High Power Products

Schottky Rectifier, 3.5 A



- Popular D-PAK outline
- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS directive 2002/95/EC
- AEC-Q101 qualified

DESCRIPTION

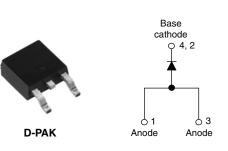
The 30WQ04FNPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

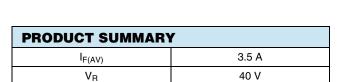
MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	3.5	А	
V _{RRM}		40	V	
I _{FSM}	t _p = 5 μs sine	500	A	
V _F	3 Apk, T _J = 125 °C	0.49	V	
TJ		- 40 to 150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	30WQ04FNPbF	UNITS	
Maximum DC reverse voltage	V _R	40	V	
Maximum working peak reverse voltage	V _{RWM}	40	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	$I_{F(AV)}$ 50 % duty cycle at T _C = 135 °C, rectangular waveform		3.5	
Maximum peak one cycle non-repetitive surge current	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	500	A
See fig. 7		10 ms sine or 6 ms rect. pulse		80	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 1 \text{ A}, L = 16 \text{ mH}$		8.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	А

For technical questions, contact: diodestech@vishay.com





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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	3 A	• T _J = 25 °C	0.53	v
		6 A		0.67	
		3 A	- T _J = 125 °C	0.49	
		6 A		0.62	
Maximum reverse leakage current See fig. 2	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	2	mA
		T _J = 125 °C		24	
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.34	V
Forward slope resistance	r _t			37.33	mΩ
Typical junction capacitance	CT	V_{R} = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 °C		189	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

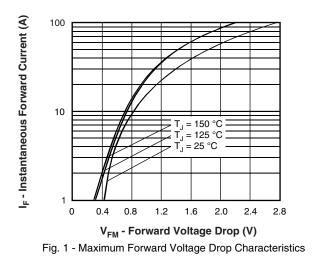
THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 40 to 150	°C
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	4.7	°C/W
Approximate weight			0.3	g
Approximate weight			0.01	oz.
Marking device Case style D-PAK (similar to TO-252AA) 30W		30WC	04FN	

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink



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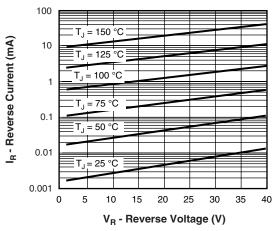


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

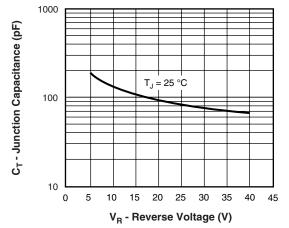


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

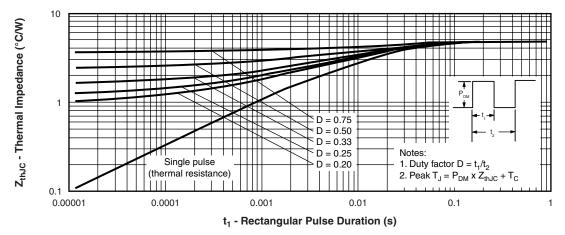
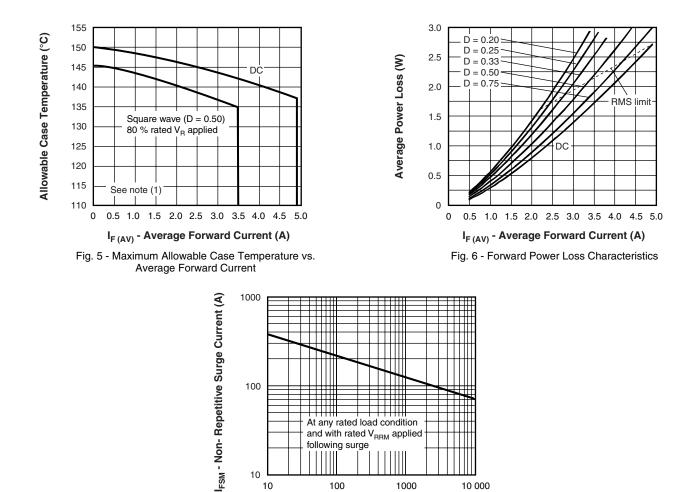


Fig. 4 Maximum Thermal Impedance Z_{thJC} Characteristics

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At any rated load condition and with rated $\mathrm{V}_{\mathrm{RRM}}$ applied

t_p - Square Wave Pulse Duration (μs) Fig. 7 - Maximum Non-Repetitive Surge Current

1000

10 000

following surge

100

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC};$ $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 D); I_R at V_{R1} = 80 \% rated V_R$

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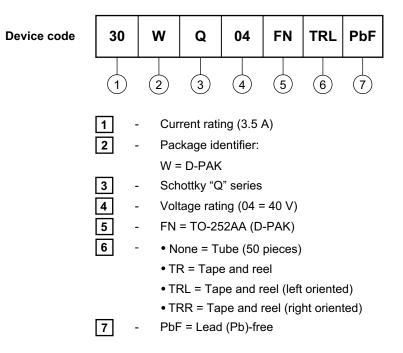
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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95016		
Part marking information	www.vishay.com/doc?95059		
Packaging information	www.vishay.com/doc?95033		
SPICE model	www.vishay.com/doc?95288		



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